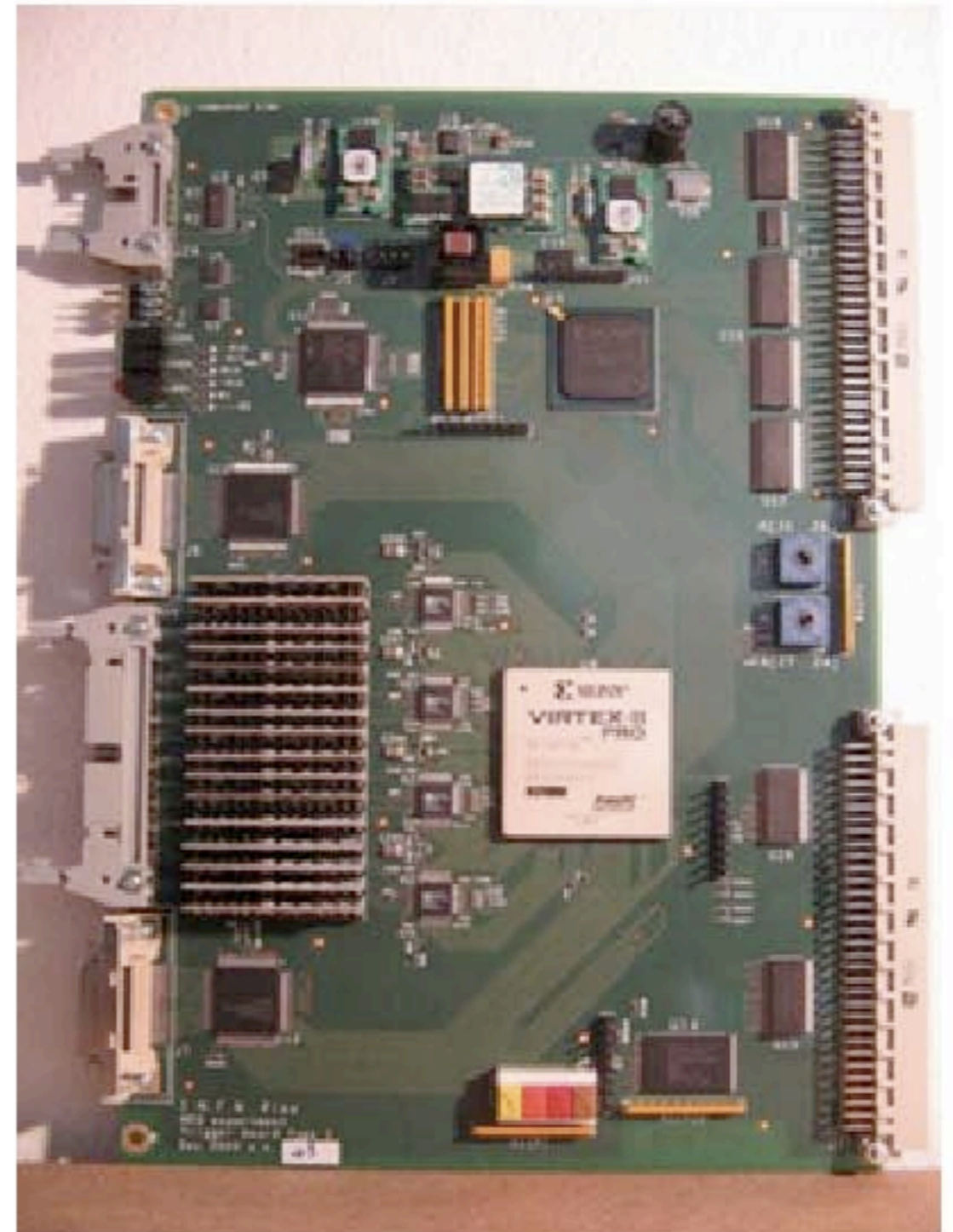


MEG trigger electronics type 1 board

- 16 analog signals on the Analog Receiver boards
- Converts the analog signal amplitudes to digital numbers at 100 MSPS , 10 bits
- Pre - elaborates the digitized amplitudes in FPGA
- Sends the elaborated information to a couple of LVDS serialiser.



MEG trigger electronics type 3 board

- Modified version of type 1 board
- Receives 32 analog on 32 Analog Receivers
- Converts the analog signal amplitudes to digital numbers at 100 MSPS, 10bits
- Stores the digitized waveforms in cyclic memories.
- Costs ~2000 Euro/board

