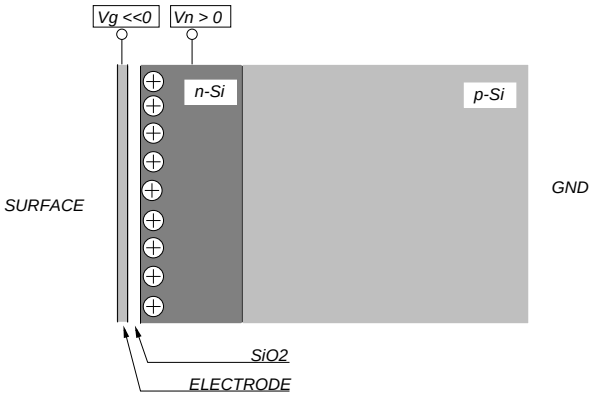
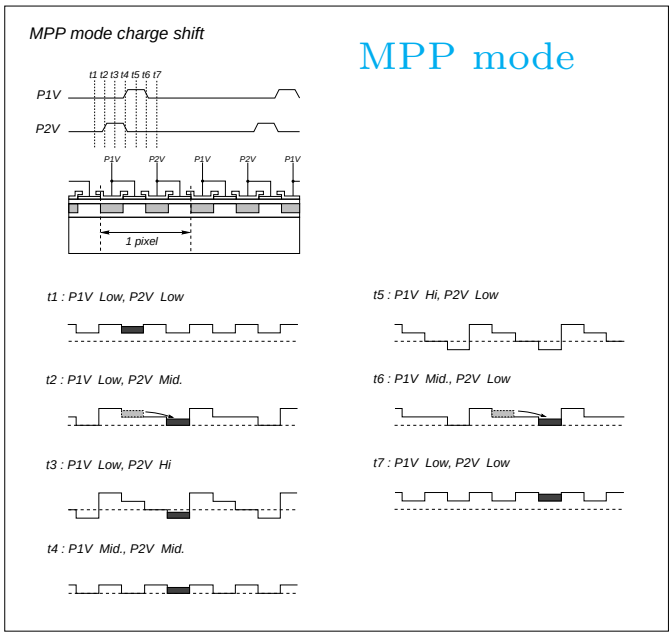
MPP (Multi Pinned Phase) Operation

"Inverted Operation" in other words



- Holes are accumulated under S_i/S_iO_2 interface.
- Thermal excitation of e^- is significantly suppressed.

$\Rightarrow$ **Reduction of the dark current by factor 10**



- 2 phase CCD: potential wells even when $P1V = P2V$

HPK S5466/C5934 achieves MPP operation