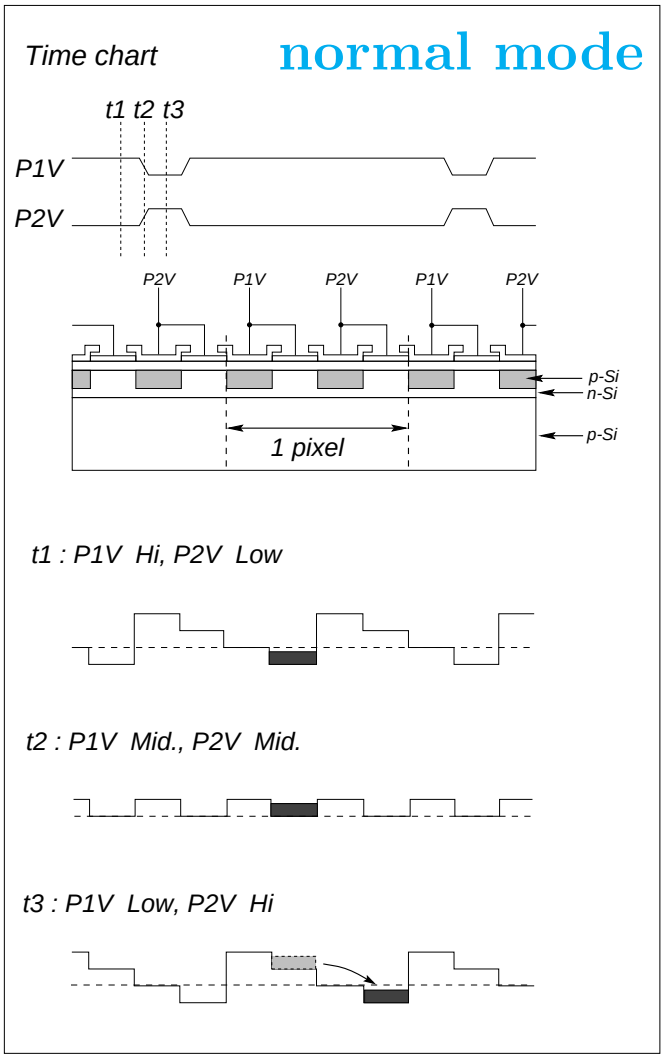
# Vertical Structure

## 2 phase CCD

- $p$ implants inside the $n$-type layer collecting phase $\leftrightarrow$ barrier phase
- Potential wells exist even when all the gate voltages are the same ($\rightarrow$ MPP operation)